

**INDIAN INSTITUTE OF TECHNOLOGY KANPUR
DEPARTMENT OF COMPUTER SCIENCE & ENGG**

To _____
M/S _____

**IITK/CSE/2018-19/26
1st June 2018**

Sub. Quotation for FPGA Development Boards

We are interested in purchasing the following board Qty-1 along with the below mentioned software having the following specs. You are requested to send your sealed quotations along with compliance report, for the below listed product, as per given specifications. The envelope must be inscribed with **“Quotation for FPGA Board”**. Last date to submit your bids is **25th June 2018 by 1500 hrs.**



FPGA

Altera Stratix® V GX FPGA (5SGXEA7N2F45C2)

FPGA Configuration

On-Board USB Blaster II or JTAG header for FPGA programming

Fast passive parallel (FPPx32) configuration via MAX II CPLD and flash memory

Memory



Memory

Two Independent DDR3 SODIMM Socket, Up to 8GB 800 MHz or 4GB 933 MHz for each socket

Four Independent 550MHz SRAM, 18-bits data bus and 72Mbit for each
256MB FLASH

Communication



Communication Ports

Four SFP+ connectors

PCI Express (PCIe) x8 edge connector (includes Windows PCIe drivers)

One RS422 expansion header

Others

General user input / output:

4 LEDs

1 LED Array

4 push-buttons

4 slide switches

2 seven-segment displays

SMA clock input / output

On-Board Clock

50MHz Oscillator

Programmable oscillators Si570, CDCM61001 and CDCM61004

System Monitor and Control

Temperature sensor

Fan control

Power

PCI Express 6-pin power connector, 12V DC Input

PCI Express edge connector power

Mechanical Specification

PCI Express standard height and 3/4-length

*** Software: Quartus software with Stratix V compilation support ***

All the installation work is included

Terms & Conditions as per below mentioned link:

https://www.cse.iitk.ac.in/doc/Annexure1_General_rules_for_purchase_in_cse_20180205.pdf

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